

This listing of claims will replace all prior versions and listings of claims in the application.

Listing of Claims:

Claim 1 (currently amended). A driving circuit for solving color dispersion, implemented in a flat panel display with a plurality of display cells, the driving circuit comprising:

a coding unit, to generate a plurality of encoded data according to a plurality of characteristic curves;

a reference voltage generator, to receive the encoded data, convert the encoded data from digital to analog, and generate a plurality of reference voltages; and

a driving unit, to receive the reference voltages and accordingly drive the display cells;

wherein the plurality of characteristic curves are Gamma curves respectively for three primary colors R, G, B; and the coding unit generates the plurality of encoded data according to the Gamma curves respectively for three primary colors R, G, B at the same time;

wherein the reference voltage generator comprises: a plurality of sample/latch circuits having inputs connected exclusively to the coding unit and arranged to receive the encoded data and apply the encoded data received to sample/latch processing for output; and a plurality of digital-to-analog converters, each having digital-to-analog converter being respectively connected to one of the sample/latch circuits by a plurality of control signal lines, to perform digital to analog conversion according to the encoded data which is outputted by the sample/latch circuit and received by the control signal lines, thereby obtaining the reference voltages.

Claim 2 (canceled).

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Claim 3 (canceled).

Claim 4 (previously presented). The driving circuit as claimed in claim 1, wherein each digital-to-analog converter inputs the encoded data through the control signal lines.

Claim 5 (canceled).

Claim 6 (canceled).

Claim 7 (original). The driving circuit as claimed in claim 1, wherein the driving unit is a data driver.

Claim 8 (canceled).

Claim 9 (previously presented) The driving circuit as claimed in claim 1, wherein the reference voltage generator further comprises:

a plurality of buffers, to receive the reference voltages from the digital-to-analog converters, enhance their output amplitudes, and output the reference voltages enhanced to the driving unit.